

DEMOCRATIZING AI

Carlos Morales, Intel AI Products Group

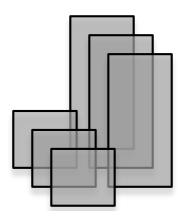
Intel has a long tradition of democratizing compute

by

Making it **easier**

Making it **powerful**

Making it **accessible**



Mainframes a few thousand users



Intel has a long tradition of democratizing compute

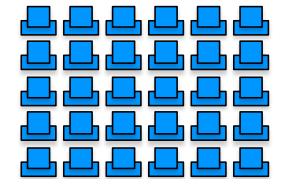
by

Making it easier

Making it **powerful**

Making it **accessible**





Personal Computers millions and millions of users

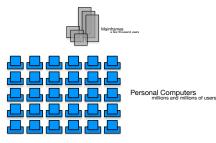


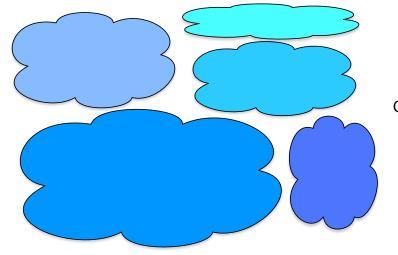
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Cloud Computing billions of users

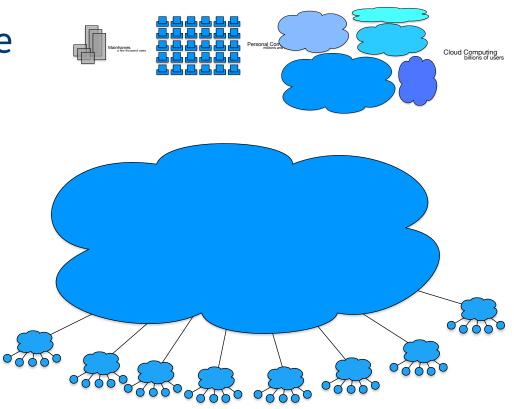


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Fog and IoT Computing so many billions of users



Intel has a long tradition of democratizing compute

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Making it easier

Making it **powerful**

Making it **accessible**





What does that actually mean?

Making it easier



Automating and abstracting anything that is not Al

Making it **powerful** Making it **accessible**



What does that actually mean?

Making it easier

Making it **powerful**



Automating and abstracting anything that is not Al

Enabling scale up, scale out and novel AI techniques for *everyone*

Making it **accessible**



What does that actually mean?

Making it easier

Making it **powerful**

Making it **accessible**

by

Automating and abstracting anything that is not Al

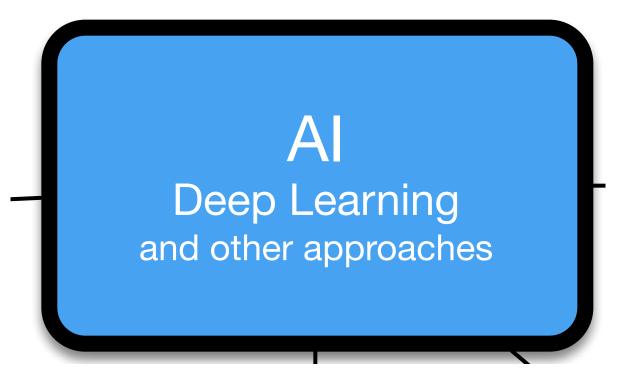
Enabling scale up, scale out and novel AI techniques for *everyone*

Bringing it to the compute platform you already have







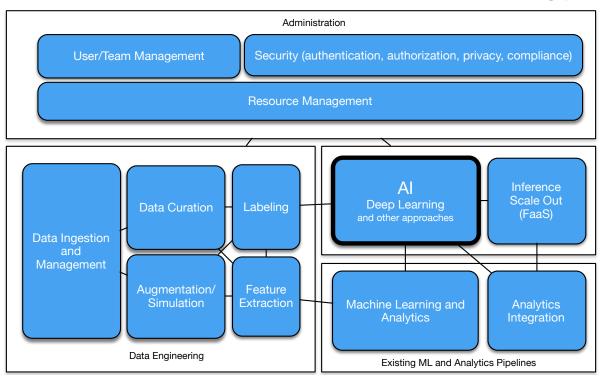


by



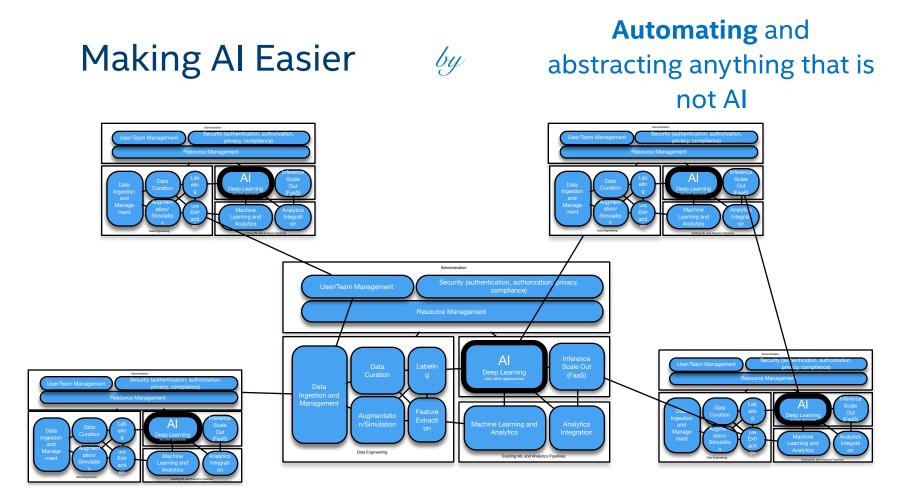


Automating and abstracting anything that is not Al



by





(intel) Al





How do we solve messy problem?

by





How do we solve messy problem?

by

The open source community, with Intel's support, is converging on solutions.

DLaaS offerings are flourishing

Kubernetes is the API



What does that actually mean?

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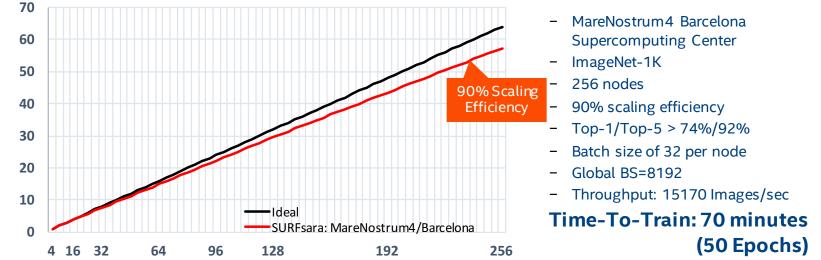


Making it Powerful Xeon Democratizes AI



Enabling scale up, scale out and novel AI techniques for *everyone*

Intel[®] - SURFsara* Research Collaboration - Multi-Node Intel[®] Caffe ResNet-50 Scaling Efficiency on 2S Intel[®] Xeon[®] Platinum 8160 Processor Cluster



Configuration Details 2:Slide 127

Performance estimates were obtained prior to implementation of recent software patches and firmware updates intended to address exploits referred to as "Spectre" and "Meltdown." Implementation of these updates may make these results inapplicable to your device or system. Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance of that product when combined with other products. For more complete information visit: http://www.intel.com/performance Source: Intel measured as of June 2017

Optimization Notice: Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microprocessors for optimizations reparative to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

Xeon Democratizes Al



Intel® Xeon® Platinum 8180 Processor higher Intel optimized Caffe Resnet50 with Intel® MKL inference throughput 133X and training throughput 73X compared to Intel® Xeon® Processor E5-2699 v3 with BVLC-Caffe

Inference and training throughput measured with FP32 instructions. Inference performance with INT8 is expected to be higher

Al performance is constantly improving with hardware and software optimizations on Intel[®] Xeon[®] Scalable Processors

INFERENCE using FP32 Batch Size Caffe GoogleNetv1 256 AlexNet256 Configuration Details on Configs: 18,2

Software and workbadsused in performance lests may have been optimized for performance only on hield microprocessors. Performance lests, su dh as SYS mark and MobileMark are measured using specific computer systems, components, software, operations and fundions. Any change be anyo flowe ladors may cause the results by vary. You should consult ofter information and performance lests to assist you in fully evaluating your contemplated purchases, including the performance of that products when components, software, operations and fundions. Any change be anyo flowe ladors may cause the results by vary. You should consult ofter information and performance lests to assist you in fully evaluating your contemplated purchases, including the performance of that products when components, software, operations and fundions. Any change be anyo flowe in the measured using specific computer systems, computer systems, components, software, operations and fundions. Any change be anyo flowe interview of the performance software to the measured as of June 2017 Optimizations in dude SSE2, SSE3, and SSSE3 instruction sets and offer optimizations. In the does not guarantee the availability, functionality, or efficience of anyoptimization on microprocessors of experiments does specific to interview of the hier optimizations in this product are intended for use with Intel microprocessors. Certain optimization sets are not incluse to the specific to the interview of the hier optimizations.



Xeon Democratizes AI: Case Study



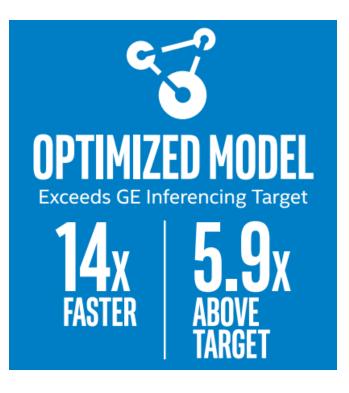
Intel's Solution Stack includes

Intel® Xeon® Scalable processors

Intel[®] Solid State Drives

Intel Deep Learning Deployment Toolkit

Intel[®] Math Kernel Library for Deep Neural Networks



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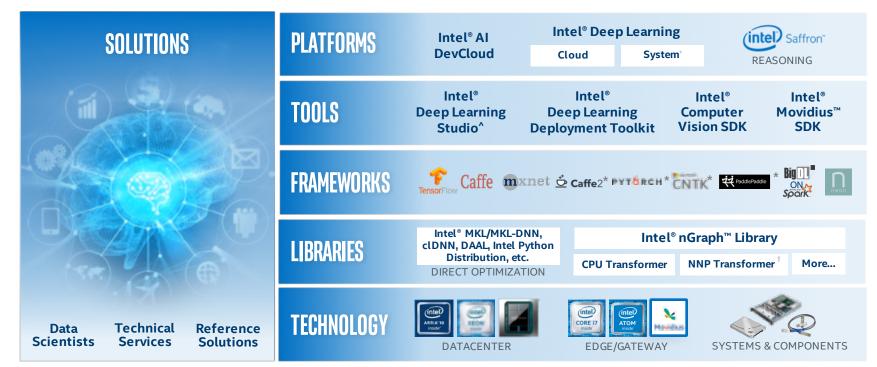
Optimizing Xeon AI Augmenting Xeon with a broad compute portfolio Enabling End-to-end AI

And most importantly

Making it easier to leverage the full stack







*Future product *Beta available ^Available in the Intel® Deep Learning Cloud, coming to other platforms later Other names and brands may be claimed as the property of others.

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INTEL[®] NERVANA[™] NEURAL NETWORK PROCESSOR (NNP)[¥]

Scalable acceleration with best performance for intensive deep learning

(intel)	Nervana"	8

PARALLELISM	SCALABILITY	UTILIZATION	ROADMAP
Massively- parallel compute	Large on-die memory	Direct SW control for best on-chip	First silicon in 2017
Specialized on-die fabrics	High speed interconnects	memory usage Managed	Product roadmap on track to exceed performance goal ¹
Optimized numerics - Flexpoint	Massive inter-chip data transfer	data-flow paths	

¥ Formerly coden amed as the Crest Family

 $\label{eq:source:https://newsroom.intel.com/news-releases/intel-ai-day-news-release/?_ga=2.26542141.1088441208.1508441324-198894050.1498491572.$

All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice.

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that product when combined with other products. For more complete information visit: http://www.intel.com/performance. Source:Intel measured or estimated as of November 2017



PROJECT BRAINWAVE FOR REAL-TIME AI

"A major leap forward in both performance and flexibility for cloud-based serving of deep learning models." Doug Burger Distinguished Engineer





INTEL IS DEMOCRATIZING AI



INTEL IS DEMOCRATIZING AI

by

Offering edge-to-edge AI compute solutions

Developing key AI software with the open source community

Making it work better together

and



cintel® Al Thank you!

Notices and Disclaimers

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Tests document performance of components on a particular test, in specific systems. Differences in hardware, software, or configuration will affect actual performance. Consult other sources of information to evaluate performance as you consider your purchase. For more complete information about performance and benchmark results, visit <u>www.intel.com/benchmarks</u>.

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